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Keshab K.Parhi, VLSI Digital Signal Processing Systems, Design and VLSI Digital Signal Processing Systems design and 6. S. Brown, R . Francis, J

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CMOS VLSI DESIGN MEVD-102 COURSE VLSI Digital Signal Processing Systems: (Architecture, Programming, Interfacing and System Design), Pearson Education. 9.

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real time computation of discrete trigonometric B.A. Bowen and W.R. Brown,Systems Design, Volume II of VLSI Systems Design for Digital Signal

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MV-201 Digital VLSI Design 3-1 4 MV-202 Digital Signal Allan Y. Oppenheim & Ronald W. Schafer , "Digital Signal Processing Design Automation of VLSI Systems,

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VLSI Systems Design. Fall Semester, 2012 Design, implementation, and fabrication of very large scale integration (VLSI) circuits. System timing analysis.

### **Faculty profile - stephen p deweerth**

" A Neuromorphic VLSI System for Modeling the Neural Control of Axial Analog and Digital Signal Processing, 46(7):967 Senior Design & Capstone Design

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1 DESIGN OF AN FFT PROCESSOR Erik Nordhamn, [Bowe85] Bowen B. A. and W. R. Brown: Systems Design, Volume II of VLSI Systems Design for Digital Signal Processing,

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The University of Texas at Dallas. VLSI Design ; Advanced Digital Logic ; Microprocessor Systems ; Computer Architecture ; Digital Signal Processing ; Analog

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, "Developing and Distributing Component-Level VHDL Models", Journal of VLSI Signal Processing, Digital Design Course", Fifth W. Bruce, J. C. Harden, and

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Very-large-scale integration. CMOS VLSI Design: A Circuits and Systems Perspective, Fourth Edition. Introduction to VLSI systems.

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### **Faculty electrical & computer engineering -**

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